**Things to discuss:**

* **Swap to 48-pin Eurocard connector**
* **Trace/space for DAC BGA**
  + **Current spacing rules**
    - **4mil traces**
    - **405um trace-to-ball**
    - **405um trace-to-trace**
    - **405um trace-to-pad**
  + **Recommended spacing**
    - **3.937 mil (100um) traces**
    - **405um trace-to-ball**
    - **433um trace-to-trace**
    - **433um trace-to-pad**
* **DACHEALTH net**
  + **Downscales SiPMBias31 to 0-5V ADC range**
  + **Is ~20MΩ of voltage divider going to be too noisy?**
  + ***Shoot for 100uA at 20V=2mW***
* **DAC REF\_IN**
  + **Current +3.3V reference sets full scale output to 0-165V**
  + **Do we need a +4V reference to set to 0-200V or is voltage divider OK?**
* ***DAC channels 25-31 – TODO: route to backplane***
* **Silk-screen layout**